

Question No: 1 (Marks: 1) - Please choose one

Which one of the following is the memory organization of SRC processor?

- $2^8 * 8$ bits
- $2^{16} * 8$ bits
- **$2^{32} * 8$ bits (Page 46)**
- $2^{64} * 8$ bits

Question No: 2 (Marks: 1) - Please choose one

Type A format of SRC uses -----instructions

- **Two (Page 47)**
- three
- four
- five

Question No: 3 (Marks: 1) - Please choose one

The instruction -----will **load** the register R3 with the contents of the memory location M [PC+56]

- Add R3, 56
- lar R3, 56
- **ldr R3, 56 (Page 47)**
- str R3, 56

دنیا میں سب سے مشکل کام اپنی اصلاح اور سب سے آسان کام دوسروں پر نکتہ چینی کرنا ہے

Question No: 4 (Marks: 1) - Please choose one

Which format of the instruction is called the accumulator?

- 3-address instructions
- 3-address instructions
- 2-address instructions
- **1-address instructions (Page 32)**
- 0-address instructions

Question No: 5 (Marks: 1) - Please choose one

Which one of the following are the **code size** and the **Number of memory bytes** respectively for a 2-address instruction?

- 4 bytes, 7 bytes
- **7 bytes, 16 bytes (Page 36)**
- 10 bytes, 19 bytes
- 13 bytes, 22 bytes

Question No: 6 (Marks: 1) - Please choose one

Which operator is used to name registers, or part of registers, in the Register Transfer Language?

- **:= (Page 66)**
- &
- %
- ©

Question No: 7 (Marks: 1) - Please choose one

The transmission of data in which each character is self-contained units with its own start and stop bits is -----

- **Asynchronous** [Click here for detail](#)
- Synchronous
- Parallel
- All of the given options

Question No: 8 (Marks: 1) - Please choose one

Circuitry that is used to move data is called -----

- **Bus** [click here for detail](#)
- Port
- Disk
- Memory

خدا کے سوا کسی سے امید مت رکھو

Question No: 9 (Marks: 1) - Please choose one

Which one of the following is **NOT** a technique used when the CPU wants to exchange data with a peripheral device?

- Direct Memory Access (DMA).
- Interrupt driven I/O
- Programmed I/O
- **Virtual Memory (Page 268)**

Question No: 10 (Marks: 1) - Please choose one

Every time you press a key, an interrupt is generated. **This is an example of**

- **Hardware interrupt (Page 275)**
- Software interrupt
- Exception
- All of the given

Question No: 11 (Marks: 1) - Please choose one

The interrupts which are pre-programmed and the processor automatically finds the address of the ISR using interrupt vector table are

- Maskable
- Non-maskable
- Non-vectored
- **Vectored (Page 277)**

Question No: 12 (Marks: 1) - Please choose one

Which is the last instruction of the ISR that is to be executed when the ISR terminates?

- **IRET (Page 278)**
- IRQ
- INT
- NMI

Question No: 13 (Marks: 1) - Please choose one

If NMI and INTR both interrupts occur simultaneously, then which one has the precedence over the other

- **NMI (Page 279)**
- INTR
- IRET
- All of the given

بری صحبت سے تنہائی بہتر ہے اور تنہائی سے نیک صحبت بہتر ہے

Question No: 14 (Marks: 1) - Please choose one

Identify the following type of serial communication error condition:

The prior character that was received was not still read by the CPU and is over written by a new received character.

- Framing error
- Parity error
- **Overrun error** (Page 240)
- Under-run error

Question No: 15 (Marks: 1) - Please choose one

-----the device usually means reading its status register every so often until the device's status changes to indicate that it has completed the request.

- Executing
- Interrupting
- Masking
- **Polling** [click here for detail](#)

Question No: 16 (Marks: 1) - Please choose one

Which I/O technique will be used by a sound card that may need to access data stored in the computer's RAM?

- Programmed I/O
- Interrupt driven I/O
- **Direct memory access(DMA)** [Click here for detail](#)
- Polling

Question No: 17 (Marks: 1) - Please choose one

For increased and better performance we use _____ which are usually made of glass.

- Coaxial Cables
- Twisted Pair Cables
- **Fiber Optic Cables** (Page 390)
- Shielded Twisted Pair Cables

Question No: 18 (Marks: 1) - Please choose one

In _____ if we find some call party busy we can have provision of call waiting.

- **Delay System** (Page 381)
- Loss System
- Single Server Model
- None of the given

اللہ کا خوف سب سے بڑی دانائی ہے

Question No: 19 (Marks: 1) - Please choose one

In ____ technique memory is divided into segments of variable sizes depending upon the requirements.

- Paging
- **Segmentation (Page 365)**
- Fragmentation
- None of the given

Question No: 20 (Marks: 1) - Please choose one

For a request of data if the requested data is not present in the cache, it is called a _____

- **Cache Miss (Page 358)**
- Spatial Locality
- Temporal Locality
- Cache Hit

Question No: 21 (Marks: 1) - Please choose one

An entire _____ memory can be erased in one or a few seconds which is much faster than EPROM.

- PROM
- Cache
- EEPROM
- **Flash Memory (Page 356)**

Question No: 22 (Marks: 1) - Please choose one

_____ chips have quartz windows and by applying ultraviolet light data can be erased from them.

- PROM
- Flash Memory
- **EPROM (Page 356)**
- EEPROM

Question No: 23 (Marks: 1) - Please choose one

The _____ signal coming from the CPU tells the memory that some interaction is required between the CPU and memory.

- **REQUEST (Page 350)**
- COMPLETE
- None of the given

دنیا کی سب سے بڑی فتح نفس پر قابو رکھنا ہے

Question No: 24 (Marks: 1) - Please choose one

_____ is a combination of arithmetic, logic and shifter unit along with some multiplexers and control unit.

- Barrel Rotator
- Control Unit
- Flip Flop
- **ALU** (Page 347)

Question No: 25 (Marks: 1) - Please choose one

In Multiple Interrupt Line, a number of interrupt lines are provided between the _____ modules.

- **CPU and the I/O** (Page 283)
- CPU and Memory
- Memory and I/O
- None of the given

Question No: 26 (Marks: 1) - Please choose one

The data movement instructions _____ data within the machine and to or from input/output devices.

- Store
- Load
- Move
- **None of given** (Page 141)

Question No: 27 (Marks: 1) - Please choose one

CRC has ----- overhead as compared to Hamming code.

- Equal
- Greater
- **Lesser** (Page 329)
- None of the given

Question No: 28 (Marks: 1) - Please choose one

The _____ is w-bit wide and contains a data word, directly connected to the data bus which is b-bit wide memory address register (MAR) .

- Instruction Register(IR)
- memory address register (MAR)
- **memory Buffer Register(MBR)** (Page 350)
- Program counter (PC)

ایماندار کو غصہ دیر سے آتا ہے اور جلدی دور ہو جاتا ہے

Question No: 29 (Marks: 1) - Please choose one

In _____ technique, a particular block of data from main memory can be placed in only one location into the cache memory .

- Set Associative Mapping
- **Direct Mapping (Page 360)**
- Associative Mapping
- Block Placement

Question No: 30 (Marks: 1) - Please choose one

_____ indicate the availability of page in main memory.

- Access Control Bits
- Used Bits
- **Presence Bits** [click here for detail](#)
- None of the given